Our company is hiring for a staff design engineer. To join our growing team, please review the list of responsibilities and qualifications.

## Responsibilities for staff design engineer

- Own and drive Roadmaps for complete IP / Subsystem domains portfolio within global R&D team
- Own and Lead IP / Subsystem from Concept till IP Design and Development achieving final design performance in integrated system within aggressive, market driven schedules
- Ensure quality adherence during all stages of the IP development cycle and carry out a thorough analysis of existing processes, recommend and implement the process improvements to ensure 'Zero Defect' designs
- Ability to work well as part of a team of global and local experts to influence and build technological innovations
- Self starter with 2-10 years of experience on SOC/Chip level/IP Timing closure and Signoff of high speed complex design with multiple clocks and power domains with minimal supervision
- Expertise in developing and owning full chip Timing Constraints for a complex, multi-clock, multi-voltage SoCs

## Qualifications for staff design engineer

- Experience with 65nm, 40nm and 28nm technologies from multiple foundries
- Proficiency in Tcl and Perl scripting
- Expertise in ICC, PrimeTime, StarRC-XT, Formality, Calibre and Redhawk or equivalent
- Knowledge and/or experience with other implementation EDA tools preferred