



Example of Silicon Validation Engineer Job Description

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Our innovative and growing company is hiring for a silicon validation engineer. If you are looking for an exciting place to work, please take a look at the list of qualifications below.

Responsibilities for silicon validation engineer

- Participate in validation and characterization of the CPU core, SOC and high-speed IOs - PCIe, ICI, USB, SATA and Power management
- Work in the pre-silicon simulation and emulation environments to ensure successful silicon bring-up
- Experience with scripting languages like python, perl or expect is a plus
- C/C++ programming, Linux, device drivers, embedded software is a plus
- Experience with test equipment, logic/protocol analyzers, and oscilloscopes are a plus
- Electrical characterization of high speed IO to support device production release
- Conduct Electrical characterization and margin testing against specification and across PVT
- Characterize silicon SERDES TX jitter and RX jitter tolerance, mask compliance, and bit error rate in lab test beds
- Understand standards for these I/O interfaces and requirements for meeting these standards
- Help in the process of Phy tuning in conjunction with internal and 3rd party designers

Qualifications for silicon validation engineer

- 10+ years of product engineering leading complex digital, Mixed Signal and

- Any ATE coding experience and SoC design knowledge would be helpful
 - ◆ Excellent communication and leadership skill
- Experience in working on ARM core architectures would be an advantage
- Knowledge of scripting in Lua, PERL and python will be beneficial
- Interested in writing code close to the hardware, and debugging