



# Example of Silicon Validation Engineer Job Description

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Our company is hiring for a silicon validation engineer. We appreciate you taking the time to review the list of qualifications and to apply for the position. If you don't fill all of the qualifications, you may still be considered depending on your level of experience.

## Responsibilities for silicon validation engineer

- Develop and continuously improve post-silicon test content that exercises deep learning workloads at both IP and SoC level
- Validating the functionality of new and existing architectural features on silicon
- Debugging, root causing, fixing, and validating pre and post-silicon issues and bugs based on broad understanding of the product multiple system areas
- Providing feedback for future on-die debug features
- As a part of your work you are expected to create, define and develop system validation environment and test suites
- Drive related schedule, programming guides, customer escalations, manufacturing yield improvements and design feedback
- Coordinate with logic design, circuit design, board design, Simulation, diagnostics, ATE, firmware, driver and marketing teams
- Develop new methodologies to improve the silicon validation process and take it to the next level
- Responsible for SSD and Storage Chip Validation
- Design and implement meaningful test plan(case) to assure ASIC quality

## Qualifications for silicon validation engineer

- Bachelor's or Master's degree in EE or CS with 5+ years of experience
- Experience with image sensor processing hardware pipelines
- Knowledge of image formats and color spaces
- Embedded software experience is a must