



Example of Silicon Validation Engineer Job Description

Powered by www.VelvetJobs.com

Our growing company is looking to fill the role of silicon validation engineer. Please review the list of responsibilities and qualifications. While this is our ideal list, we will consider candidates that do not necessarily have all of the qualifications, but have sufficient experience and talent.

Responsibilities for silicon validation engineer

- Demonstrate results with technical solutions and deploy across MC D organization
- Escalate technical issues and risks, with appropriate data, to management to take immediate actions
- Designers, Concept Engineers, Project Managers, and Program Managers across MC
- Ownership of post-silicon validation of system including both HW and SW
- Develop and execute post-silicon test plans by running SW on silicon
- Develop software to test, evaluate and stress the silicon and system
- Responsible for booting, silicon bring-up, debug, and device characterization
- Contribute to debug architecture to enable efficient debug of silicon
- Understand intended applications and stress systems from a customer perspective
- Working with architects, design and pre-silicon validation engineers to define post-silicon validation plans

Qualifications for silicon validation engineer

- A minimum of three years of experience developing verifications collateral in Verilog, System Verilog and OVM/UVM
- A precise attention to details

- Lab skills, debug silicon issue on system level, tune design parameters cross parts and corners
- Algorithm validation through simulation and spreadsheet analysis
- Experience in CPLD/FPGA designs using Verilog