



Example of Process Integration Engineer Job Description

Powered by www.VelvetJobs.com

Our company is searching for experienced candidates for the position of process integration engineer. If you are looking for an exciting place to work, please take a look at the list of qualifications below.

Responsibilities for process integration engineer

- Leading and managing a direct and matrix reporting group of engineers responsible for developing the etch process technology roadmap, including process architecture, process capability, process roadmap, tool capability and roadmap and extendability, silicon experimentation and analysis
- Develop expertise in parametric tests, probe flow, circuit operation
- Design, develop, and improve wafer fab process for new product development and implementation
- Work with R&D and Fab engineering teams closely to integrate wafer fab processes to generate and validate wafer process flows for new product developments
- Set up various wafer Fab processes with emphasis on statistical process control (SPC)
- Optimize wafer process for manufacturability and device performance and yield improvement
- Definition of the pixel architecture and corresponding process integration schemes
- Working closely with other teams to define the specs and requirements, run feasibility study, improve the existing processes, design, implement, and analyze the electrical data of DOEs (Design of Experiment) to recommend the best possible methods for the existing and future products
- Work closely with global partners to stay current on module related topics and process alignment projects

Qualifications for process integration engineer

- Thin film deposition/characterization experience a plus
- Demonstrated ability to contribute to or lead local team activities virtual, global cross-functional teams
- Several years of semiconductor process experience in process integration, lithography or OPC/DFM
- You should have a solid working knowledge of modern CMOS process integration, with some exposure to Front-end-of-line (FEOL) integration
- Ideally your profile is rounded up by your knowledge in programming languages, SVRF, TCL or SQL
- Several years of semiconductor process experience in multiple process modules, Defect Inspection or process integration desired, experience in advanced technology is preferred