Our company is growing rapidly and is looking for a pre-silicon validation engineer. To join our growing team, please review the list of responsibilities and qualifications.

Responsibilities for pre-silicon validation engineer

- Functional validation tests to verify system will meet design requirements
- Creation of test plans for RTL validation
- Define and run system simulation models
- Finding and implementing corrective measures for failing RTL tests
- Debug failing tests
- Work with designers and architects to resolve bugs
- Working with Performance Architecture team to come up with a performance test plan
- Develop methodologies and techniques to drive performance validation to meet product requirements
- Collaborate with architects, hardware engineers, and ucode engineers to understand the new features being implemented, create test plans and functional coverage metrics
- Validation of an IP at the IP or system level

Qualifications for pre-silicon validation engineer

- Ability to communicate well with counterparts and key stakeholders including multinational partners
- BS in electrical engineering or computer engineering, and at least three years professional experience
- 5-8 years of experience in validating multi-million gate IP/Subsystem/SoC

- Understanding of Image Processing, Machine Vision fundamentals, and algorithms HOG, HAAR would be an advantage
- Planning Test strategy, creating test plan and test code development to address functional and performance requirements of the IP/Subsystem