Our innovative and growing company is looking for a pre-silicon validation engineer. If you are looking for an exciting place to work, please take a look at the list of qualifications below.

Responsibilities for pre-silicon validation engineer

- Analyze, interpret and assess HW and FW architectural specifications to define security requirements for IP/SoC designs
- Review new IP/SoC HW/FW architecture to drive upstream design development to meet product security objectives
- Contribute to downstream post-silicon and platform security validation actively participate in customer security matters
- Building multiple emulation targets for an SoC
- Adding support for new features/IPs into existing emulation models
- Drive/Participate in discussions across various disciplines to get a clear understanding of requirements
- Develop/run/debug tests in SystemVerilog
- Actively review code created by fellow team mates
- Track progress of self/subteam to achieve goals timely
- Provide indicators and guidance to management on issues and roadblocks on a timely basis

Qualifications for pre-silicon validation engineer

- Experience in pre-silicon validation (2-6 years)
- Proven capabilities in strong RTL coding (Verilog MUST
- Significant Experience in PCIE
- Opportunity to drive for methodology and technology enhancements that

- Knowledge in RTL integration and validation methodologies is a plus