Our company is growing rapidly and is looking to fill the role of pre-silicon validation engineer. To join our growing team, please review the list of responsibilities and qualifications.

Responsibilities for pre-silicon validation engineer

- Development of verification collateral (such as behavioral checkers, coverage monitors, test generators or score-boards) is often required to enable test plan execution
- Use scripting skills to enable tool development and automation for verification efficiency
- Pre-Silicon validation of Image Processing Subsystem
- Development of validation Strategy, Plans, Coding and Debug of scenarios on Pre-Si Validation platform
- Development & bring-up of Pre-Silicon Validation platforms like Palladium/ZeBU/Veloce or FPGA for the IP/SOC Validation
- Development and execution of system use case scenarios
- Writing software to provide controllability and observability into the architectural mode
- As a Pre/Post Silicon Validation Engineer, you are required to create, define and develop system validation environment & test suites
- You are responsible for the development of methodologies, execution of validation plans, and debug of failures
- You are responsible in validating the functionality of new architectural features of next generation designs by developing test plans, tests content, coverage points or test tools

Qualifications for pre-silicon validation engineer

- Experience of working on SoC projects with a proven track record of successful first rime delivery of projects
- Knowledge of C/C++, SystemC, Perl, TCL, Shell scriptingl/Tk/Perl
- Knowledge of Software paradigms such as Polymorphism and Inheritance
- Define and enhance methodologies for pre-silicon validation of high complexity IP/SoC designs improving the overall efficiency and velocity of the pre-silicon validation team
- Interact closely with the architecture and design teams, influencing product definition, implementation and validation