Our growing company is searching for experienced candidates for the position of logic design engineer. Please review the list of responsibilities and qualifications. While this is our ideal list, we will consider candidates that do not necessarily have all of the qualifications, but have sufficient experience and talent.

Responsibilities for logic design engineer

- Design of chip layout circuit design, circuit checking, device evaluation and characterization
- Perform developmental and/or test work, review product requirements and logic diagrams
- Work with a senior front end team to define and implement new RTL features
- Ensure the logic design meets architectural specifications
- Contribute to the micro-architectural feature specification
- Implement block and sub-block designs using SystemVerilog
- Balance design trade-offs between modularity, scalability, power, area, and performance
- Collaborate with the pre-silicon validation team to develop functional test plans
- Collaborate with the physical design team to converge the front end design for a clean hand-off, with emphasis on timing of critical structures
- Engage in early high-level architectural exploration, through micro architectural research and arriving at a detailed specification

Qualifications for logic design engineer

- Contributes to Eurobalise and S-HMI data preparation for ETCS
- Contributes to the elaboration of trackside layout plans for ETCS Subsystem
- Utilizes specific SW tools for implementation of ETCS Subsystem

- Maintains a strong link with ERTMS architects, System Engineers and HW Designers
- Applies processes and standards