

## **Example of Lead Design Engineer Job Description**

Powered by www.VelvetJobs.com

Our growing company is searching for experienced candidates for the position of lead design engineer. To join our growing team, please review the list of responsibilities and qualifications.

## Responsibilities for lead design engineer

- Lead problem resolution activities for structural components within the battery and interfacing structures
- Support structures DREs to ensure that their components meet program imperatives
- Design and Release RESS structural components as workload dictates
- Sign off and track any deviations in builds from plan for structural components
- Generic knowledge on network, Web services and IT tools will be an added advantage
- Significant contribution to client- or R&D-projects for the whole project lifecycle (definition to successful delivery), explicitly during the definition and implementation phase
- Provides day-to-day administrative and technical supervision to a group of product engineers to execute development projects and provide comprehensive technical support related to product design for SLI lead acid batteries
- Proficient in PDM/PLM Tools Pro/e
- Interpret customer needs (internal and external depending upon work area)
  to identify the relevant modifications and enhancements required to the
  product
- Use engineering software (Creo) to analyse problems and issues to determine the appropriate solutions and modifications required

- Proficiency in engineering design software (Civil 3D, Navisworks, Revit)
- Minimum 5 years of experience with racing or race teams
- The candidate should have a good experience in developing and optimizing core signal processing algorithms (for video and imaging domain) in C and assembly-language/intrinsic programming for a DSP or DSP-like processor
- The candidate should have a good knowledge of processor ISA/architecture for at least 1-2 DSP and should have a very good grasp of the issues surrounding software performance tuning for the given DSP ISA/architecture
- The candidate should have a good experience in developing and optimizing DSP applications in C and assembly language for a DSP or DSP-like processor, and should have a good grasp of the issues surrounding software performance tuning
- Will have demonstrated successful completion of 5+ design projects as an individual contributor