



Example of Layout Engineer Job Description

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Our growing company is looking for a layout engineer. Please review the list of responsibilities and qualifications. While this is our ideal list, we will consider candidates that do not necessarily have all of the qualifications, but have sufficient experience and talent.

Responsibilities for layout engineer

- Research and analyze customer requests, industry standards, and specifications to help define page layout and map book functionality
 - Develops and prepares multi-dimensional layouts and detailed drawings of the semiconductor devices from schematics and related geometry provided by design engineering
 - Plant layout management and optimization
 - Block level ownership of layout, unit level verification, layout design reviews
 - Keeping up to date CAD drawings (both production area and offices)
 - Managing improvement projects on site
 - Working closely with all teams on site improving production
 - Engage with Technology Development, Process Integration, Metrology, Photolithography, Characterization, Data Preparation, Design Services, and Mask House teams for specifications to design electrical and optical designs overall frame structure to be developed
 - Use or customize Process Design Kits (PDKs) other physical design tools, to generate electrical and optical designs assemble an overall frame structure, to enable reticle level designs to be defined
 - Verify the accuracy of electrical and optical designs overall frame structure using Design Rule Checking (DRC) tools or similar custom scripts and work with the specification owners to gain approval by the Waiver Review Board (WRB) when applicable
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- Bachelor's degree in Engineering (GPA of 3.0 or higher)
- Be part of a team of designers and verification engineers, working closely with other team members to implement and verify the functionality of a given design element within the context of the block, chip and overall system to design features for the next generation in RTL
- Lead the documentation and executing of any features that the candidate is assigned to
- Bachelor or Master of Science Degree in Electrical or Computer Engineering
- Familiar with UVM methodology
- Familiar with C/C++ language