



# Example of FPGA Design Engineer Job Description

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Our company is growing rapidly and is looking to fill the role of FPGA design engineer. Please review the list of responsibilities and qualifications. While this is our ideal list, we will consider candidates that do not necessarily have all of the qualifications, but have sufficient experience and talent.

## Responsibilities for FPGA design engineer

- Requirements-based test bench development for the verification of RTL blocks using VHDL or System Verilog
  - Work with a multi-disciplinary design team to translate customer needs into a flexible test and measurement solution architecture
  - Perform detailed design work, including RTL design for FPGAs, component selection, schematic design, and more
  - Provide risk assessments, suggest and execute feasibility investigations, and provide guidance on bringing new technology to market
  - Participate in customer visits and technical conferences to help identify new technologies and propose new ideas to add to our technology and product development funnels
  - Designs, develops, debugs, modifies, and tests designs by using current tools, analysis techniques, and technologies
  - Documents development by writing documents, reports, memos, change requests
  - Tracks development effort by creating and maintaining records in the approved tracking management tool
  - Participate in interdisciplinary design teams and reviews such as peer reviews, code reviews, and other technical exchanges
  - Interface with HW, FW, and SW engineers to test and verify electrical interfaces and protocols between the FPGA and embedded system devices
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- Strong logic/board debug, and analytical skills
- US Citizenship, and ability to obtain/maintain clearance required
- Be the technical expert on next generation real time video processing technologies and interfaces
- Design, verification and maintenance of high bandwidth data pipelines in FPGA hardware
- Design, verification and maintenance of control, IO and state in FPGA hardware
- Be the technical expert on SSD technologies and PCIe interfaces