Our company is hiring for a design engr. To join our growing team, please review the list of responsibilities and qualifications.

Responsibilities for design engr

- Working with ASIC designers and architects to produce thoroughly verified, robust IP
- Overseeing all verification for the IP/Chip/System and driving functional and code coverage closure
- Actively participating in post-silicon bringup, validation and compliance testing
- Actively participating in cross functional collaboration with design, software and hardware teams to ensure a successful product delivery
- Mentor other engineers and technically guide them
- Development of ASIC verification and Validation plans
- Development of UVM and C test cases for ASIC verification and validation to achieve comprehensive coverage
- Working with ASIC design and architecture teams to understand functionality of logic blocks of SSD controller
- Comprehending "big picture" at the ASIC architectural and system level executing at the detail block level
- Supports the process of identifying and scoping toll-free data analytical projects that align with priority business opportunities, challenges, and needs

Qualifications for design engr

- Have solid knowledge of design principles and practices
- Has a strong desire to learn and explore new cutting edge technologies
- Have working knowledge advanced design tools

methodology and tools

- Networking and packet based protocol experience is desired
- Have design knowledge of Phase Locked Loops (PLL), Crystal Oscillators (XOSC), bias generators, reference circuits, Low Drop-Out regulators (LDO) and DC-DC converters, high speed/general IOs, Data converters