Our growing company is hiring for a design engineer staff. We appreciate you taking the time to review the list of qualifications and to apply for the position. If you don't fill all of the qualifications, you may still be considered depending on your level of experience.

Responsibilities for design engineer staff

- Drive primary design configuration between commonly used printed dipole and inverted F antennas, and provide potential alternatives that meet unusually challenging customer requirements and can efficiently be produced via high-volume manufacturing methods
- Ensure that reliability needs are met in the design for EMC, safety, manufacturability and regulatory requirements
- Evaluate and deploy the evolving verification methodologies to handle increasingly complex IP/SubSystem designs within aggressive, market-driven schedules
- Own and ensure quality adherence during all stages of the project cycle
- Hands on and ability to work well as part of a team both locally, and with remote or multi-site teams
- FC clocking
- Layout Verification- Reliability Verification
- Electrical Rule Verification

Qualifications for design engineer staff

- Expertise in Verilog/System Verilog, C/C++/SystemC, OVM, UVM, Scripting languages like Perl/Python
- Highly motivated, reliable
- Good communicator, personable, team player

- Extensive experience in modern digital ASIC methodologies including front-end design and verification, synthesis, STA and signal integrity, DfT and physical implementation
- Experience in leading digital IC design teams