Our company is looking to fill the role of design engineer staff. We appreciate you taking the time to review the list of qualifications and to apply for the position. If you don't fill all of the qualifications, you may still be considered depending on your level of experience.

Responsibilities for design engineer staff

- Develop firmware for memory chip operations
- Validate and debug design features on silicon
- Work with manufacturing and test teams to ensure product delivery
- Design optimized digital blocks in verilog meeting design specifications
- Run simulations to debug and verify the design
- Work with verification engineers to develop test bench and verification methodology using verilog, system verilog, and UVM
- Work with physical design team on synthesis, verification and static timing
- Implement DFT and develop test vectors for production test
- Collaborate with system engineers, analog engineers, application engineers, and test engineers on digital specifications, analog testability, and debugging
- Write and hold detailed design reviews

Qualifications for design engineer staff

- BSc degree in Electrical/Electronic/Computer Engineering with focused IC design content (Masters degree preferred, PhD welcomed)
- 2 to 6 years of experience in IC circuit design, preferably for optical and/or wireline applications
- Robust foundation of analog circuit theory, transmission lines and device fundamentals
- Proven drive for results with contributions to high performance, high speed

- Preferred experience of HFSS, ADS and microwave modeling
- Ability to interpret lab verification results to support applications and device characterization