Our innovative and growing company is hiring for an ASIC engineer. We appreciate you taking the time to review the list of qualifications and to apply for the position. If you don't fill all of the qualifications, you may still be considered depending on your level of experience.

Responsibilities for ASIC engineer

- You will develop effective flow to improve IP team's productivity
- You will need maintain the health of project working tree, and auto regression system
- You will need manage the usage of farm resource
- Codes and programs enhancements, updates, and changes for portions and subsystems of systems software, including operating systems, networking, utilities, databases, and Internet-related tools
- Triage and troubleshoots failures down to the root cause
- Engage early with architecture team and design team to gain understanding of system architecture and module design specs
- Map ASIC design into model design and implement by SystemC/TLM2
- Test bench and test case Design and Unit verification in SystemC/TLM2
- Integrate modules into complete SSD system
- Verify the system's basic functions with testing Firmware written in C/C++/Assembly

Qualifications for ASIC engineer

- Problem solving skills and out-of-the-box thinking to test and validate hardware designs write reusable UVM/C++ classes for various simulation environments
- Knowledge of verilog, system-verilog, synopsys, ASIC flow is a must

- 6+ months working with scripting languages (perl, tcl,) to enhance design automation, verification, and delivery
- Knowledge of synthesis and static timing analysis
- The candidate must posses a Bachelor's degree in Electrical Engineering, Computer Engineering or a related discipline