



Example of ASIC Engineer Job Description

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Our company is hiring for an ASIC engineer. We appreciate you taking the time to review the list of qualifications and to apply for the position. If you don't fill all of the qualifications, you may still be considered depending on your level of experience.

Responsibilities for ASIC engineer

- Proactively create educational materials and product documentation
- Definition, documenting and execution of development plans to design/verify complex block or sub-system level designs (250K to 5M+ gates)
- Responsible for definition, design, verification and documentation for ASIC (Application Specific Integrated Circuit) and/or FPGA (Field Programmable Gate Array) developments
- Verifies test results and analyzes performance
- Contributes to the generation and maintenance of work products
- Develops and presents requirements, concepts, designs, decisions and results to internal management, other organizations, team mates and customers
- May contribute to technical subcontract management that may include SOW development, proposal evaluation, source selection, technical oversight, and subcontractor work product evaluation and acceptance
- Reviews vendor capability to support product development
- Applies a strong understanding of the organizationally defined processes throughout the lifecycle of the program or project
- Participates in the improvement of the ASIC/FPGA organizational processes

Qualifications for ASIC engineer

- Great Object oriented programmer in Perl, C++
- The ideal candidate will have a combined knowledge of formal methods

development, and a good understanding of hardware design and micro architecture

- Understand OCC and DFT is a plus
- Understand SystemVerilog/UVM is a plus
- Effective schematic capture, and behavioral modeling of circuits in Verilog, logical equivalence verification between schematic and Verilog models
- Physical verification of circuit through layout-extracted static timing analysis, electrical rules' check and noise analysis