

Example of ASIC Engineer Job Description

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Our growing company is looking for an ASIC engineer. Please review the list of responsibilities and qualifications. While this is our ideal list, we will consider candidates that do not necessarily have all of the qualifications, but have sufficient experience and talent.

Responsibilities for ASIC engineer

- Design, Simulate, and/or Evaluate RTL and real silicon supporting product development
 - Participate in functional and micro-architectural discussions with design team
 - Develop verification plan for module and/or chip-level verification based on design specs. Conduct initial test plan review, test object review, and exit reviews
 - Develop testbench tests, including models, drivers, monitors, and checkers
 - Gather and improve code coverage and functional coverage with designer's feedback
 - Provide mentoring and guidance for other team members in verification methodology and tool usage
 - Responsible for developing automated ASIC and FPGA verification strategies using Verilog and high level verification tools
 - Implementation of electronic design integration and finishing tools and flows using a combination of custom scripting, vendor tool APIs, and C/C++ programming
 - Implementation of circuit level checking and model
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 - Implementation of circuit level checking and model generation tools and flows
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- Familiar with LCD/LVDS/DisplayPort/HDMI, vertical/horizontal scalar and color space is a big plus
- Perl scripting skills is appreciated as a plus
- Video/Camera/ImagePostProcessing/ComputerVision related experience is a big plus
- Experience in Formal Verification is a Plus
- Understanding of BIOS, drivers and other software applications a plus
- Low power ASIC design understanding a plus