



# Example of ASIC Engineer Job Description

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Our company is growing rapidly and is hiring for an ASIC engineer. We appreciate you taking the time to review the list of qualifications and to apply for the position. If you don't fill all of the qualifications, you may still be considered depending on your level of experience.

## Responsibilities for ASIC engineer

- Develop Perl, Tcl and/or shell scripts to enhance current verification infrastructure/methodology
- Perform micro-architecture and logic design to deliver maximum throughput, while using minimum power
- Lead Soc implement/integration
- Bring-up/Improve the flow for synthesis, audit, Formal check, static timing check and DFT
- Implement RTL vs SYN netlist formal check and netlist vs netlist formal check
- Bring up the SDC for PT, support timing closure, power estimation, power domain division criteria
- Support DFT related problems (such as ATE patterns debug) and post-simulation
- Work with architect and designer to develop test plan
- Work with physical designer to fix timing closure
- Take SOC implement/integration with high quality

## Qualifications for ASIC engineer

- Familiar with ASIC flows (logic synthesis, STA, formality check, Design for Power ) and usage of related EDA tools
- Familiar with script languages((tcl, perl ) in unix/linux
- Prior experience in PCI Express or similar protocols is a plus

- Project experience in IC design implementation