Our company is searching for experienced candidates for the position of analysis engineer. Please review the list of responsibilities and qualifications. While this is our ideal list, we will consider candidates that do not necessarily have all of the qualifications, but have sufficient experience and talent.

Responsibilities for analysis engineer

- Simulate and evaluate performance of active and passive thermal control systems for future NASA vehicles and habitats including the Orion spacecraft and vehicles capable of lunar or Mars missions
- Correlate thermal models to test results
- Develop thermal models of space hardware or test equipment using Thermal Desktop analysis code and first principal models with standard desktop software
- Promote MBUSA's reputation for cooperation and good corporate citizenship by assisting/supporting the FBI, local law enforcement agencies, NHTSA, when appropriate
- Failure analysis will be performed on failures from wafer fabrication or package assembly, reliability stress test failures, qualification fails, customer returns new product and process introduction fails
- This position involves the utilization of deprocessing analytical equipment including topside & backside decapsulation, layer by layer removal using wet and dry etching the use of analytical techniques including Scanning Electron Microscopes (SEM), Focus Ion Beam (FIB), Confocal Laser Scanning Microscope (CLSM), Photon Emission Microscopy (EMMI), Thermal Induced Voltage Alterations (TIVA), and Energy Dispersive X-rays (EDX)
- Responsibilities include the generation of high quality Product Analysis Reports to the internal engineering and manufacturing staff customers
- Must have detailed understanding of semiconductor physics, MOS & bipolar

- Will drive quality improvement and provide corrective action recommendations conduct hands-on failure analysis activities
- Train others in physics of failure, analytical techniques and methodologies benchmark FA methods, systems and operations against other semiconductor manufacturers

Qualifications for analysis engineer

- Well versed with the various systems, tools, and procedures used in failure analysis
- Understand component SPECIFICATION and schematic diagram of the modular
- The applicant must have expertise in debugging using tools such as IDA, Softice, Ollydbg
- Strong knowledge of analysis flows and methodology
- Knowledge of all aspects of ASIC physical design including floorplanning, clock and power distribution
- Familiarity with package modeling techniques for chip level power analysis