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# Example of Principal Product Engineer Job Description

Our innovative and growing company is hiring for a principal product engineer. Please review the list of responsibilities and qualifications. While this is our ideal list, we will consider candidates that do not necessarily have all of the qualifications, but have sufficient experience and talent.

## Responsibilities for principal product engineer

* Contributes to design risk management activity, primarily DFMEAs
* Leads issue resolution activity through effective root cause analysis, solution mapping and assessment, and effective corrective action
* Contributes to device requirements management, including identifying technical requirements, an ability to identify gaps and missing information, and the timely evolution of requirements
* Contributes to project scheduling and budgeting
* Represents department on cross-functional product development teams
* Fulfills all requirements applicable to Product Development Engineer responsibilities within the Quality Management System
* Manages a wide variety of technical tasks and projects in the development of new products and manufacturing processes
* This individual will be a leader within the New Product Introduction group and a key member of Acclarent’s medical device product development teams
* Serves as a core team member and supply chain representative on the Acclarent new product development teams
* Contributes to and develops strategy for new product concepts, processes and techniques to ensure that products meet customer’s needs and are high quality and low cost products

## Qualifications for principal product engineer

* Key interface to internal and external customers for Temperature products and applications
* 7+ years experience developing, debugging, or deploying memory controller IP or products
* 3+ years experience developing or using high speed DDR interface is a plus
* Experience with DDR PHY interface IP (DDR3/DDR4, LPDDR3/LPDDR4)
* Experience finding creative solutions and workarounds to functional bugs and process issues
* Experience with Verilog, synthesis, and static-timing analysis