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# Example of Layout Design Job Description

Our company is looking for a layout design. Please review the list of responsibilities and qualifications. While this is our ideal list, we will consider candidates that do not necessarily have all of the qualifications, but have sufficient experience and talent.

## Responsibilities for layout design

* Perform fully-customized test structure layouts for pixel designs and verification
* Clean database by using physical verification tools such as DRC, LVS and ANT
* Develop test document for different test structure designs
* Monitor electrical test parameters and debug issues, if any
* Assist in the performance of product development activities, which includes drawing board layout
* Assist in the interpretation and implementation of customer specifications and documentation requirements
* Assist in the application of engineering principle knowledge to conduct routine analyses and/or tests pertaining to the development of new designs, methods, materials or processes
* Assist in conducting engineering studies
* Verification (DRC, LVS, ERC) and debug using the latest CAD tools
* Chip-level assembly, optimization and verification

## Qualifications for layout design

* Quantifying power and performance improvement in popular benchmark and game applications (like 3D Mark, Firestrike, Battleman, ) with state-of-art modeling methodology
* Feedback and driving key architectural decisions such as cache size, clock frequency, chip level power domain partition
* Knowledge in SoC performance analysis like bandwidth, latency
* Familiar with Verilog HDL, Tcl, Shell, Perl programming
* Familiar with popular EDA tools
* Knowledge of UPF/CPF-based low power design/flow is a plus