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# Example of Design Enablement Job Description

Our growing company is looking to fill the role of design enablement. Thank you in advance for taking a look at the list of responsibilities and qualifications. We look forward to reviewing your resume.

## Responsibilities for design enablement

* Design implement key block designs at prototype level and production quality on internal test chip schedules
* Engage with subsystem leads critical elements of circuitry, work with layout engineer for overall layout
* Layout electrical verification and modification to meet specifications
* Characterize initial silicon and work with external test resources for comprehensive testing of sufficient quantities of ICs to demonstrate achievement of critical performance requirements
* Key focus is to maintain high quality PDK enablement for GF's RF-AMS Foundry technologies and clients
* Developing Standard Cell design specifications and PPA targets based on Market and Product Requirement Documents
* Defining Standard Cells (or Embedded memories) architecture and features that are competitive at both IP and SOC level
* Reviewing and approving SOW for standard cell (or Embedded Memories) development
* Working with Technology and Fab Teams on device level specs to optimize Standard cells (or Embedded Memories) in term of PPA
* Reviewing and dispositioning technology change requests which may impact the IP

## Qualifications for design enablement

* Technical knowledge in the areas of physical chip architecture, chip floorplanning, clock distribution, power gating, physical verification, timing, noise and power analysis
* Experience – Physical design, Synopsys or Cadence synthesis and route tools
* BS in Computer Science, Electronics Engineering or related \* 5 years of relevant industry experience FPGA, CPU, GPU, DSP, ASIC, or extensive digital design \* Experience with the use of FPGA development tools
* In pursuit of a BS Degree in Electrical Engineering, Device Physics, Material Science, Semiconductor Process, Computer Engineering or other relevant engineering or physical science discipline is required
* Master degree in Electronics/Microelectronics
* Good knowledge of Linux operating system, embedded OSes