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# Example of ASIC / Layout Design Engineer Job Description

Our growing company is hiring for an ASIC / layout design engineer. We appreciate you taking the time to review the list of qualifications and to apply for the position. If you don’t fill all of the qualifications, you may still be considered depending on your level of experience.

## Responsibilities for ASIC / layout design engineer

* The successful candidate will work with team members and apply current functional verification techniques to perform and improve pre-silicon verification quality and product Time to Market for central DFD IP
* Develop innovative architectural improvements to optimize performance per millimeter, performance per watt, and performance per byte
* Develop features to enhance visual quality and immersive environments
* Develop new features for use cases enabling entrance into emerging markets and build stronger positions in current markets
* Develop architectural and micro-architectural specifications
* Develop test, test plans, infrastructure to validate performance, power and software
* Conduct present tradeoff studies to select best methods in a timely fashion
* Review the design specs with design team for the design verification plan
* Create test plan, create test env., create tests based on the test plan
* Run/debug verificaton tests, clean up the env/test case issues

## Qualifications for ASIC / layout design engineer

* Experience with SystemVerilog, OVM, perl, Design Patterns a plus
* Background and interest in computer graphics a plus
* Major in EE, CS or related, Master Degree experiences
* Knowledge on computer architecture and PCIe devices is preferred
* Good knowledge on verification methodologies like UVM is a big plus
* Master’s Degree in Electrical or Computer Engineering preferred