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# Example of ASIC / Layout Design Engineer Job Description

Our company is looking to fill the role of ASIC / layout design engineer. Thank you in advance for taking a look at the list of responsibilities and qualifications. We look forward to reviewing your resume.

## Responsibilities for ASIC / layout design engineer

* Write ASIC specific part of test plan
* Participate in making functional/technology based chip targets in timing, area, power
* Responsible for IP development and maintenance
* Involves collaboration on or assuming the responsibility for a specific project, or a definable portion of a larger project, process or client group
* Includes accountability for results in that area, and for communication of the status of interdependencies to management or senior design staff
* Develop infrastructure and environment for IP/SoC level design verification
* Developing Testbenches and Verification Components such as UVCs, models, BFMs, and Re-usable Verification Environments
* Overseas definition, design, verification, and documentation for ASIC development
* Responsible for multiple aspects in PD areas and provide technically leadership to the engineering team
* Aaccountable for project delivery

## Qualifications for ASIC / layout design engineer

* Video coding and decoding design and DV experience is desirable
* Bachelor with 5+ yeas and Master with 3+ in Electrical or Computer Engineering
* Experience of display specific-interconnection protocols (DisplayPort, LVDS, VGA,HDCP, DVI, HDMI etc) is a plus
* Good at C/C++, Perl, familiar with SystemC, PLI, Makefile is a plus
* BS Degree in Electrical or Computer System Engineering or Computer Science (5 years experience)
* MS (0-2 Years Experience Relevant Industry Experience)