

FPGA Cover Letter

3829 Margrett Terrace South Salena fort, IN 66517-6652

Dear Peyton Schimmel,

I submit this application to express my sincere interest in the FPGA position.

In my previous role, I was responsible for technical support, fully resolve customer issues and queries from the Intel office or on customer site.

My experience is an excellent fit for the list of requirements in this job:

- Contributes to the development of new theories and methods in ASIC/FPGA engineering
- Very strong understanding of ASIC/FPGA engineering processes
- Very strong awareness of business objectives and Engineering's role in achieving
- Very strong skill in communicating issues, impacts, and corrective actions
- Very strong understanding of basic project leadership principles including SPI/CPI, Earned Value, Cost Account Management (CAM), and Statistical Process Controls
- Provides resolution to problems to a diverse range of complex problems which require the use of ingenuity and creativity
- Considerable knowledge of high speed digital logic circuit design including use of large FPGAs and high performance memories
- Considerable knowledge of VHDL or Verilog synthesis, simulation testbenches and TCL scripts

Thank you for taking your time to review my application.

Sincerely,

Ari Grimes