FPGA Cover Letter

8107 Jesenia OvalHandside, NM 01274 **Dear Alexis Stark,**

In response to your job posting for FPGA, I am including this letter and my resume for your review.

Previously, I was responsible for technical and engineering insight and direction to support and improve usability, applicability and adoption of products, platforms and solutions to meet customer business needs.

My experience is an excellent fit for the list of requirements in this job:

- PhD in Computer Science, Electrical Engineering, or similar discipline
- Broad knowledge of digital electronics and other areas of engineering
- Eligible for security clearance to SC level
- Experience with large-scale FPGA development, specifically for Xilinx/Altera
- Experience with heterogeneous multi-processor architectures
- Proficiency in C in Windows or Linux development environments
- Video codec development experience, such as H.264 or H.265
- Schematic/PCB design

I really appreciate you taking the time to review my application for the position of FPGA.

Sincerely,

Brooklyn Durgan