FPGA Cover Letter

77873 Joana UnderpassBaumbachmouth, PA 07515-2633

Dear Ryan Beer,

I submit this application to express my sincere interest in the FPGA position.

In my previous role, I was responsible for sustaining engineering support including documentation, manual creation and internal/external technical support to ensure a smooth transition of all new product developments from engineering into production, and ensure designs are sustainable and compatible with long-term manufacturability and customer service.

Please consider my experience and qualifications for this position:

- Skills in System Verilog for verification would be highly appreciated
- Integration of the developed firmware on the target hardware with other system components and software
- Handling lab equipment such as signal generators, oscilloscopes, logic analyzers, power supplies, would be added advantages
- Knowledge of networking protocols and audio/signal processing algorithms are highly appreciated
- Experience in Audio engineering preferred
- Experience in FPGA/VHDL design/architecture
- Knowledge of ASIC design Python script development experience
- Altera or Xilinx FPGA design background Verilog/VHDL coding experience

Thank you for your time and consideration.

Sincerely,

Briar Considine