FPGA Cover Letter

38380 Kyle ExpresswayNew Ivory, MT 71308

Dear Avery Bogisich,

I submit this application to express my sincere interest in the FPGA position.

In my previous role, I was responsible for these services on both internal and externally facing applications, and interacts with customers, IT partners, system architects, product managers, design engineers, service and support teams, and other system users who require input regarding their planned or existing system.

I reviewed the requirements of the job opening and I believe my candidacy is an excellent fit for this position. Some of the key requirements that I have extensive experience with include:

- Embedded software/firmware design
- Instrument Control and Automation
- Commercial SDRs / GNU Radio
- Advanced expertise using (System) Verilog or VHDL for implementation and verification of FPGA designs
- Intermediate knowledge of video timing and interface standards
- Intermediate expertise analyzing FPGA power, performance, resources, and other tradeoffs relative to system requirements
- Advanced understanding of the internal structure of FPGAs and the behavior of FPGA design tools
- Advanced expertise writing clear, accurate, and thorough requirements, analysis and design descriptions

Thank you for taking your time to review my application.

Sincerely,

Briar Thompson