

# Design Integration Engineer Cover Letter

997 Homenick Way  
Fredericktown, ID 87640

**Dear Quinn Connelly,**

In response to your job posting for design integration engineer, I am including this letter and my resume for your review.

In my previous role, I was responsible for product innovation & development scoping of Contact Centre solutions, especially during the later stages of design & development.

Please consider my qualifications and experience:

- Industry exposure to and knowledge of ASIC/FPGA design methodology, especially logic synthesis, static timing analysis, logic equivalence checking, and working with physical design teams for floorplanning and timing closure
- Familiarity with common on-chip bus protocols such as AMBA (AXI, AHB, APB)
- Knowledgeable of chip level design and IP integration methodologies
- Experience with Verification flows and techniques at the RTL and gate level using UVM flows
- Experience with STA (static timing analysis) and modeling techniques using Primetime
- Proficiency with UNIX-based scripting languages (Perl, Python, TCL, AWK, CSH)
- Knowledge of USB and Ethernet Physical Layer protocol
- RTL design with System Verilog

**Thank you for your time and consideration.**

Sincerely,

Brooklyn Turcotte