

Design Integration Engineer Cover Letter

3218 Garfield Hill East Amiraview, CA 49327-3108

Dear Peyton DuBuque,

Please consider me for the design integration engineer opportunity. I am including my resume that lists my qualifications and experience.

In the previous role, I was responsible for expertise in PCB board design and FPGA logic to support the Mode S secondary radar beacon system life extension project (SLEP).

My experience is an excellent fit for the list of requirements in this job:

- Work with silicon and package development teams to identify suitable test vehicles in order to develop new package platforms and to evaluate the use of new semiconductor technologies in package platforms
- Drive the generation and comparison of new package concept solutions in order to determine optimum solution that should be developed
- Be involved in new package/silicon/product brainstorming and IP generation
- Understand the implications of integrating fab technologies into new package platforms and feed this into project teams
- Attend to conferences and liaise with academic establishments and vendors to identify new materials, processes and equipment that have the potential to enhance the value of our packaging platforms
- Understanding of the DRAM process flow, the function and purpose of major DRAM components, such as Sense Amp, Word-line driver, and Anti-Fuse
- Familiarity with CAD group interactions, data post-processing, and the process of transferring data from the database to the reticle
- Previous experience in R&D for DRAM (or other memory technologies) or working with-in Product Engineering

Thank you for considering me to become a member of your team.

Sincerely,